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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

First Named Applicant: Katoh)	Art Unit: 2116
Serial No.: 10/707,389			Examiner: Yanchus III
Filed:	December 10, 2003	.) .)	JP920020250US1
For:	POWER SUPPLY CONTROLLER INFORMATION PROCESSOR	AND)))	November 21, 2006 750 B STREET, Suite 3120 San Diego, CA 92101

RESPONSE TO OFFICE ACTION

Commissioner for Patents Alexandria, VA

Dear Sir:

This responds to the Office Action dated November 2, 2006, allowing Claims 1-3, 5-7, and 15-19, which is gratefully acknowledged, and rejecting/Claims 9-14 (of which 9, 10, and 14 are independent) under 35 U.S.C. §103 as being unpatentable over Ostojic, USPN 6,771,052 in view of alleged admissions of prior art (AAPA) and in view of various allegations of inherency.

The fact that Applicant has focussed its comments distinguishing the present claims from the applied references and countering certain rejections must not be construed as acquiescence in other portions of rejections not specifically addressed.

Ostojic, col. 4, lines 35-40 has been used as the claimed "state value setting combination circuit which generates a change instruction to change the combination of voltages supplied by the power supply unit". Not only that, but this over-achieving five lines from Ostojic has been used as the claimed "state value setting

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